AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

Serial Number: 10/797,770 Filing Date: March 10, 2004

Title: Fully Differential CMOS Phase-Locked Loop

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SPECIFICATION

Please amend the paragraph included in lines 3-7 of page 1 of the specification as follows:

This application is a continuation of U.S. Patent Application Serial No. 09/919,636, <u>filed on July 31, 2001, now U.S. Patent No. 6,721,380</u>, which is a continuation-in-part application of U.S. Patent Application Serial No. 09/540,243, filed on March 31, 2000, now U.S. Patent No. 6,526,113, which claims the benefit of priority from U.S. Provisional Patent Application Serial No. 60/148,417, filed on August 11, 1999, the disclosures of which are hereby incorporated by reference in its entirety for all purposes.